

Applicant(s) Application No. 10/622,061 MIYABE ET AL. Notice of Allowability Examiner Art Unit Tuan N. Nguyen -- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308. 1. This communication is responsive to 03/31/2006. 2. The allowed claim(s) is/are 1-17. 3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). b) ☐ Some* c) ☐ None 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)). * Certified copies not received: _____. Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. 4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient. 5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted. (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached 1) I hereto or 2) I to Paper No./Mail Date (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d). 6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL. Attachment(s) 5. Notice of Informal Patent Application (PTO-152) 1. Notice of References Cited (PTO-892) 6. Interview Summary (PTO-413), 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) Paper No./Mail Date 5/5/06. 7. X Examiner's Amendment/Comment 3. Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date 4. Examiner's Comment Regarding Requirement for Deposit 8. X Examiner's Statement of Reasons for Allowance of Biological Material 9. Other ____.

DETAILED ACTION

Response to Amendment

In responding to applicant's amendment filed 03/31/2006, claims 1, 17 have been 1. amended.

EXAMINER'S AMENDMENT

2. An examiner's amendment to the record appears below, to the amended claims, 1, 17 should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no latter than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview on May 5/2006 with Mr. Norman P. Soloway (Attorney for Applicant, Reg. No. 24,315).

Claim 1 (currently amended): A semiconductor laser device comprising: a heat radiating block;

at least one or more first semiconductor laser element(s) element arranged on said heat radiating block in a manner where one electrode is in direct physical contact therewith; and

at least one or more second semiconductor laser element(s) element arranged on said heat radiating block, and in an electrically insulated manner via a dielectric layer

a dielectric layer arranged to electrically insulate said first semiconductor laser element from said second semiconductor laser element, wherein said second semiconductor laser element is in physical contact with said dielectric layer.

Claim 17 (currently amended): A semiconductor laser device comprising:

a semiconductor substrate;

a first semiconductor laser element directly provided on [[a]] said semiconductor

substrate;

a second semiconductor laser element provided on in physical contact with a dielectric layer formed on said semiconductor substrate, said second semiconductor laser element being

smaller in heat generation during driving; and a photodiode provided on the surface of said

semiconductor substrate for monitoring laser output from said first semiconductor laser element

or said second semiconductor laser element, wherein said first semiconductor laser element and

said photodiode are electrically insulated by a pn structure formed on the surface of said

semiconductor substrate.

Allowable Subject Matter

3. The following is an examiner's statement of reasons for allowance - Applicant's response

filed on 03/03/2006 has been considered, with respect to claims 12, 14, 22 the references of the

record fail to teach or suggest:

Claim 1:

A semiconductor laser device comprising a first and second semiconductor mounted on a

heat radiating block, where the first semiconductor is not mounted on a dielectric layer, where

the dielectric layer insulate the first laser from the second laser, wherein said second

semiconductor laser element is in physical contact with said dielectric layer.

Claim 17: A semiconductor laser device comprising a first semiconductor laser element directly provided on a substrate, while a second semiconductor laser element in physical contact with a dielectric layer formed the substrate having smaller in heat generation, and a photodiode on the substrate monitoring the first or second semiconductor laser element, wherein said first semiconductor laser element and said photodiode are electrically insulated by a pn structure formed on the surface of said semiconductor substrate.

4. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Communication Information

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tuan N Nguyen whose telephone number is (571) 272-1948. The examiner can normally be reached on M-F: 7:30 - 4:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Harvey Minsun can be reached on (571) 272-1835. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Application/Control Number: 10/622,061 Page 5

Art Unit: 2828

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Tuan N. Nguyen

Tuan Nayye

MINSUN CH HARVEY PRIMARY EXAMINER